ARE:005RC1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Äpplicant

Peter Warnes, et al.

Appl. No.

09/523,877

Filed

March 13, 2000

For

METHOD AND APPARATUS FOR MULTI-MODE JUMP

DELAY SLOT CONTROL IN A PIPELINED PROCESSOR

Examiner

Huisman, David J.

Group Art Unit:

2183

27299
PATENT TRADEMARK OFFICE

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

January 7, 2004 (Date)

Robert F. Gazdzinski Reg. No. 39,990

AMENDMENT AND RESPONSE TO OFFICE ACTION

10

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 **RECEIVED**

JAN 1 4 2004

Technology Center 2100

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Dear Sir:

In response to the Office Action dated October 7, 2003 for the above-identified application ("Office Action"), Applicant provides the following:

01/12/2004 YPOLITE1 00000095 09523877

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